



Bharati Vidyapeeth
New Delhi

IEEE Delhi Section with Computational Intelligence Society, Computer Society, Communication Society, Consultants Network Affinity Group, Life Members Affinity Group, Inter Society Relations, Industry Relations, TEMS, SMC, ES, CI, SIGHT Standing Committees of IEEE Delhi Section and Region-10



IEEE Delhi Section

with following associations



CSI Delhi
Chapter



Computer Society Chapter
IEEE Delhi Section



IITP New Delhi



IETE New Delhi



IEEE Delhi Section

Invite you for a Free **Webinar** on **18-04-2026, the Saturday**, at **06:00 p.m.**
as per the details given here under: -

Speaker

Mr. Abhishek Kumar Singh

Principal Engineer, HARMAN India

Date and Time Schedule

18-April-2026, the Saturday, from 06:00 p.m. to 07:30 p.m.

Title of the Talk

The AI-Augmented Development Workflow: Complete SDLC with AI-Enabled IDEs & CLI-Based Assistants in Action

Venue and Contact Details

CISCO WebEx Meeting Platform

CISCO WebEx Event ID and Password shall be sent on E-Mail to the registered participants, at-least one day in advance. Please check your Spam and other folders also for our emails and mark it as SAFE / NOT SPAM.

Programme Schedule

05:30 p.m. to 06:00 p.m. – Login, online Networking and Tea (at Home)

06:00 p.m. to 07:00 p.m. – Technical Talk

07:00 p.m. to 07:30 p.m. – Q & A Session followed by Certification

Registration Link and Fee

Registration Link: <http://bvicam.ac.in/webinars/>

No registration fee. It is absolutely free for the members of IEEE, CSI, IETE, ISTE, ISCA, IITP and Bharati Vidyapeeth, with pre-registration

Abstract of the Talk

Software development is experiencing a fundamental shift — powered by AI assistants that can understand context, generate code, design architectures, and automate repetitive tasks across the entire development lifecycle. This session explores how AI-augmented workflows are transforming how software engineers build, test, and deploy applications. Through practical demonstrations participants will see how AI accelerates every phase — from High-Level Design and Low-Level Design to sequence diagrams, implementation, and testing. The session addresses the critical distinction between AI Engineers who build intelligent systems and Software Engineers who leverage AI tools to enhance productivity.

All are welcome:

Please circulate this to all your interested colleagues, associates and friends. Please register in large numbers and derive maximum benefit. **Advance registration is mandatory for participation. Participants will also get Participation Certificate.**

(Prof. M. N. Hoda)

Director, BVICAM, New Delhi

(Prof. Subrata Mukhopadhyay)

Chairperson, CNA Group, IEEE DS