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with following associations



CSI Delhi Chapter



IITP New Delhi



IETE New Delhi



IEEE Delhi Section

Invite you for a Free **Webinar** on **04-04-2026, the Saturday**, at **06:00 p.m.**
as per the details given here under: -

Speaker

Mr. Ruwan Jayanetti

CTO (Embedded Technologies), Nagarro

Date and Time Schedule

04-April-2026, the Saturday, from 06:00 p.m. to 07:30 p.m.

Title of the Talk

Evolution of the CPU: From Minuscule to Mighty AI

Venue and Contact Details

CISCO WebEx Meeting Platform

CISCO WebEx Event ID and Password shall be sent on E-Mail to the registered participants, at-least one day in advance. Please check your Spam and other folders also for our emails and mark it as SAFE / NOT SPAM.

Programme Schedule

05:30 p.m. to 06:00 p.m. – Login, online Networking and Tea (at Home)

06:00 p.m. to 07:00 p.m. – Technical Talk

07:00 p.m. to 07:30 p.m. – Q & A Session followed by Certification

Registration Link and Fee

Registration Link: <http://bvicam.ac.in/webinars/>

No registration fee. It is absolutely free for the members of IEEE, CSI, IETE, ISTE, ISCA, IITP and Bharati Vidyapeeth, with pre-registration

Abstract of the Talk

The Central Processing Unit (CPU) is the brain of any computer, responsible for executing instructions and driving all its operations. From the tiny processors in our remote controllers to the powerful CPUs in supercomputers capable of mimicking human intelligence, these intricate devices are marvels of engineering. Understanding how CPUs work, from their fundamental building blocks to their complex architectures, is crucial for anyone interested in computer science and technology. It is intriguing to know how they evolved from its historic beginning to delivering near AGI in last half century. In this webinar, we'll delve into the inner workings of the CPU, exploring its evolution, key components like the ALU and control unit, instruction sets, pipelining, caching, and the latest advancements in processor design. We'll journey from the minuscule transistors to the mighty multi-core processors that power our digital world.

All are welcome:

Please circulate this to all your interested colleagues, associates and friends. Please register in large numbers and derive maximum benefit. **Advance registration is mandatory for participation. Participants will also get Participation Certificate.**

(Prof. M. N. Hoda)

Director, BVICAM, New Delhi

(Prof. Subrata Mukhopadhyay)

Chairperson, CNA Group, IEEE DS