



BHARATI VIDYAPEETH'S

INSTITUTE OF COMPUTER APPLICATIONS & MANAGEMENT (BVICAM)

(Affiliated to Guru Gobind Singh Indraprastha University, Approved by AICTE, New Delhi)

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Course Code: MCA-107

Course Name: Computer Organization

Class Test 2

Time: 1 Hour

Max Marks: 20

Each Question carries 2 marks

1. ____ method is used to establish priority by serially connecting all devices that request an interrupt.

- (a) Polling
- (b) Daisy chaining
- (c) Priority
- (d) None of these

2. Which is commonly used in high -speed devices to realize full efficiency of communication link

- (a) Transmission
- (b) Synchronous communication
- (c) Multiprocessor
- (d) All of these

3. Which section refer to the code segment of a process that is executed when the process intends to enter its critical section

- (a) Critical section
- (b) Entry section
- (c) Reminder section
- (d) None of these

4. Which memory is assembled between main memory and CPU

- (a) Primary memory
- (b) Cache memory
- (c) Both a & b
- (d) None of these

5. Parallel computing means doing several tasks simultaneously thus improving the performance of the _____

- (a) Data system
- (b) Computer system
- (c) Memory
- (d) Vector register

6. In RISC architecture access to registers is made as a block and register file in a particular register can be selected by using

- (a) Multiplexer
- (b) Decoder
- (c) Subtractor
- (d) Adder

7. After actual locations for main storage are known, a _____ adjusts relative addresses to these actual locations

- (a) Relocating loader
- (b) Locating loader
- (c) Default loader
- (d) None of these

8. Full form of MIPS assembler is

- (a) Microprocessor without interlocked pipeline stage
- (b) Microprocessor with interlocked pipeline stage
- (c) Both a & b
- (d) None of these

9. Opcode is the machine instruction obtained from decoding instruction stored in

- (a) Stack pointer
- (b) Address pointer
- (c) Instruction register
- (d) Incrementer

10. Which of following 2 types of computer system considered by micro programmed unit

- (a) Micro level computers
- (b) Machine level computers
- (c) Both a & b
- (d) None of these

11. Design 64×1 MUX using 8×1 MUX. Draw the block diagram. Show one level circuit diagram also. (5 marks)

12. What is the value of output Z if input A is 1101 with inputs $S=1, I_r=0$ & $I_l=1$ are passed to a 4-bit shift register? Draw the respective block diagram for the same. (5 marks)