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## Director



### **Prof. M. N. Hoda**

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Director**

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**Prof. Hoda?** joined **Bharati Vidyapeeth, Pune**, on 01.09.1997 as a Lecturer at BVIMR, New Delhi and now he is working as **Professor of Computer Science & Director** at **Bharati Vidyapeeth?s Institute of Computer Applications & Management (BVICAM)**, New Delhi (under GGSIP University, New Delhi), since **01st April, 2002**. He has over twenty five years of

experience in academics in different capacities. Prior to joining the academics, he has initially worked with corporate as a Software Engineer.

He is an expert member of many board level committees of Govt. of India like Dept. of Science and Technology (DST), Council of Scientific and Industrial Research (CSIR), Ministry of Human Resources and Development (MHRD), etc. He is a member of **Academic Council** (the Apex Academic Body of the University) of GGSIP University, New Delhi, since last 12 consecutive terms from 2006. He has also served three terms as the Member of **Board of Management** (the Apex Management Body of the University) and one term as the member of the **Court** of GGSIP University, New Delhi. He is also working as the **Co-ordinator cum Convener** for MCA Programme of GGSIP University, New Delhi, since last Fifteen years from 2004.

He is the **Editor-in-Chief** of **International Journal of Information Technology** (BJIT), having ISSN 2511 - 2104 (print version) and ISSN 2511 - 2112 (electronic version), indexed with world's premiere Abstracting & Indexing (A&I) databases like **DBLP** (Germany), **INSPEC** (UK), Google Scholar (USA), etc., being published quarterly by **Springer Nature** (The world's largest publisher of scientific documents). The Journal is also enlisted in the UGC list of Approved Journals. He has edited more than 20 volumes of Conference Proceedings, as Chief Editor, having ISSN and ISBN serials. He has co-authored two books; one entitled **Handbook of MCA** for U. P. Technical University and a reference book on **Mathematical Modelling, Optimization and Their Applications** published by Narosa International. Another book entitled **The Foundation Course in Artificial Intelligence** is under process. He has published over **100 Papers** at both National and International levels, in the journals and conferences of repute, including Springer (Singapore), IEEE (USA) and IET (UK). He has participated in more than **150 Workshops / Seminars / Staff Development Programme (SDP)** organized at National and International levels as Resource Person, Key note speaker, session chairman, moderator, panelist, etc. He has delivered over **200 Invited Talks, Key Note Addresses** at different academic forums on various emerging issues in the field of Information Technology and Innovations in Teaching Learning System. He has organized and conducted over **110 National Level Management Development and Quality Improvement Programmes** for working executives in the industry and academia. As member of different Committees and Task Forces, he has facilitated more than a dozen of Universities in overall restructuring of IT and Computer Science programmes as well as innovating and re-aligning their teaching learning practices in line with the requirements of the IT industry in **21st Century**.

He has recently been elected as the **National President** (ICT Section) of the **Indian Science Congress Association (ISCA)**, Kolkata. He has been elected as the member of the **National Governing Council, The Institution of Electronics and Telecommunication Engineers (ETE)**, New Delhi, for a period of three years from 2016 and is serving as the **Chairman** of its **Technical Programme Committee**. He has served as **National Chairman (Systems)** of **Computer Society of India (CSI)** for two consecutive terms from 2013 - 2017. Prof. Hoda was elected unopposed as the **Chairman**, Indian Society for Technical Education (**ISTE**), Delhi Section for 2012 - 2014. He has also served as **Secretary cum Treasurer**, ISTE, Delhi Section, for 2009 - 2011. He was also the Regional Students' Co-ordinator (Region-1), CSI for 2011 - 2013. He was elected un-opposed as **Chairman**, Computer Society of India (CSI), Delhi Chapter for the year 2009-2010. He has also served as **Vice Chairman** for the year 2008-2009, as **Hony. Secretary** for two consecutive terms; 2006-2008 and 2005-2006. He was appointed as the **Returning Officer** for IETE Delhi Centre Elections

2012 ? 2014. He has also served as the **Returning Officer** for IETE Delhi Centre Elections 2008 ? 2010 and member, Board of Scrutinizers for 2010 ? 2012 & 2014 ? 2016.

He holds MCA degree from Aligarh Muslim University (AMU), Aligarh and Doctorate in Information System Audit in Computer Science. His current areas of research are Information System Audit, Software Engineering, Computer Networks, Artificial Intelligence, ICTs and Innovative Pedagogies for 21st Century Teaching Learning System.

He is a **Senior Member** of **IEEE (USA)**, Member of **ACM (USA)**, **Fellow** of Institution of Electronics and Telecommunication Engineers (**IETE**), **Life Member** of Computer Society of India (**CSI**), **Life Member** of Indian Society of Technical Education (**ISTE**), Life Member of Indian Science Congress Association (**ISCA**) and National HRD Network (**NHRD**). He was awarded **Outstanding Achievement Award ? 2000** by Management Studies and Promotion Institute, New Delhi for his outstanding achievement and contribution to the field of Management and Computer Education in the year 2001 and the **Best IT Teacher of the Year** during 18<sup>th</sup> All India Management Congress in 2016 for his outstanding achievement and contribution to the field of Teaching, Research and Institution Building.

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